

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. : 7,735,093 Confirmation No. 2773
Applicant : Andrew E. Gruber
Filed : March 2, 2004
Art Unit : 2194
Examiner : Van H. Nguyen
Docket No. : 091154
Customer No. : 23696

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANMITTAL ACCOMPANYING CERTIFICATE OF CORRECTION

Dear Sir:

Attached hereto is a Certificate of Correction for the referenced patent number 7,735,093. The exact page and line number where the error occurs in the application file are:

Column 10, line 17, claim 5: "processor and" to read as --processor; and--
Column 10, line 49, claim 8: "a real time event" to read as --the real time event--
Column 10, line 54, claim 8: "buffer:" to read as --buffer;--
Column 11, line 67, claim 15: "where in" to read as --wherein--

Please charge any fees or overpayments that may be due with this response to Deposit Account No. 17-0026.

Respectfully submitted,

Dated: 2011-06-03

By: /James R. Gambale, Jr./
James R. Gambale, Jr., Reg. No. 66,052
858.658.4241

QUALCOMM Incorporated
Attn: Patent Department
5775 Morehouse Drive
San Diego, California 92121-1714
Telephone: (858) 845-4265
Facsimile: (858) 658-2502